

FIG. 1

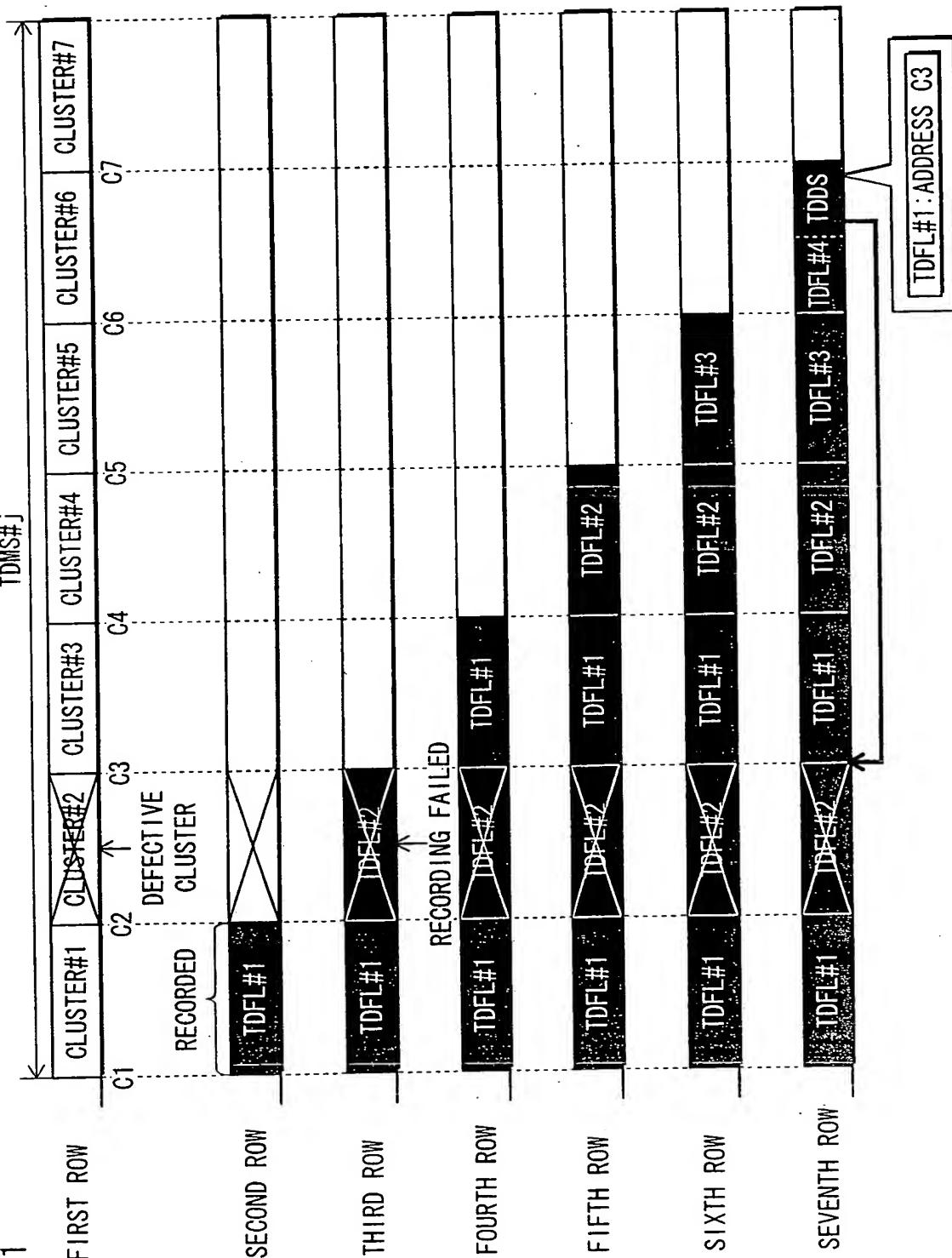


FIG. 2A

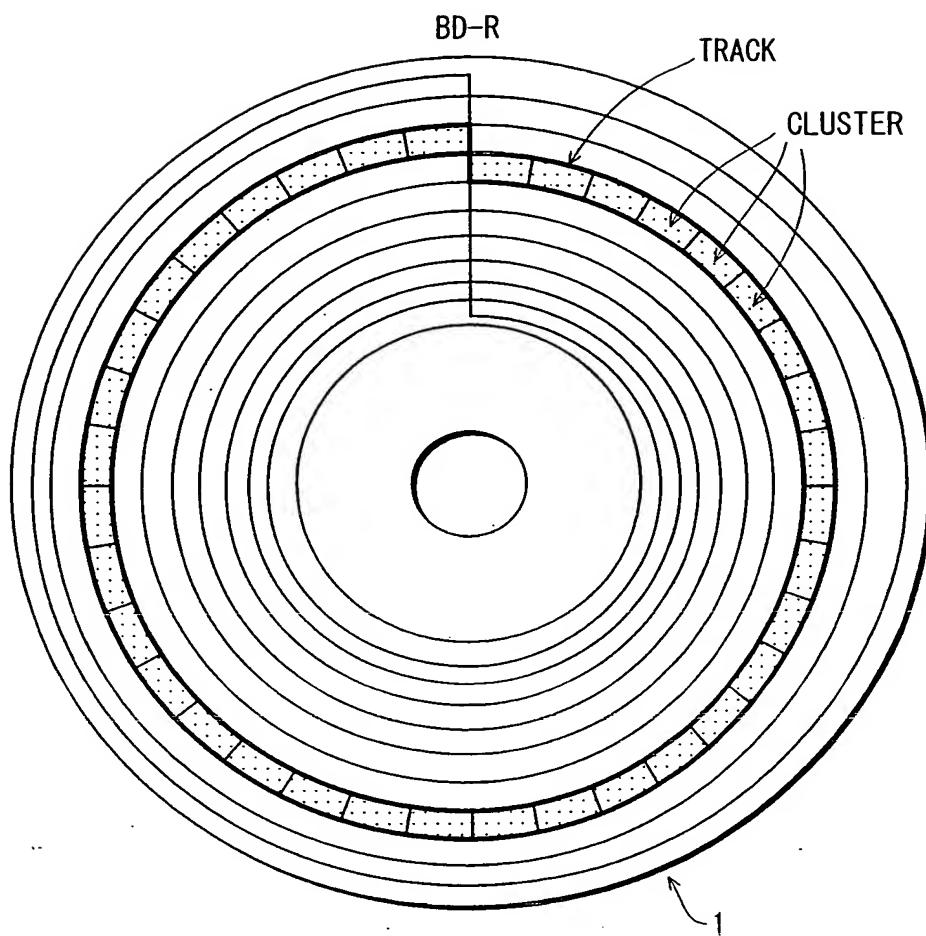


FIG. 2B

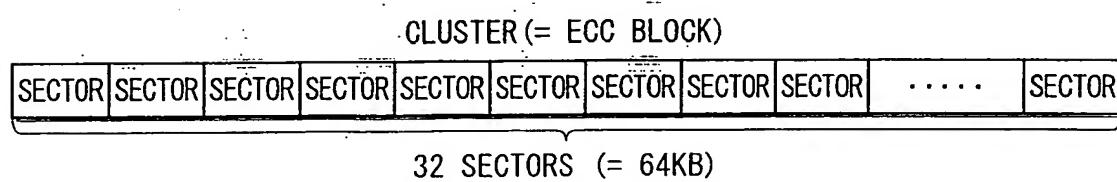


FIG. 3

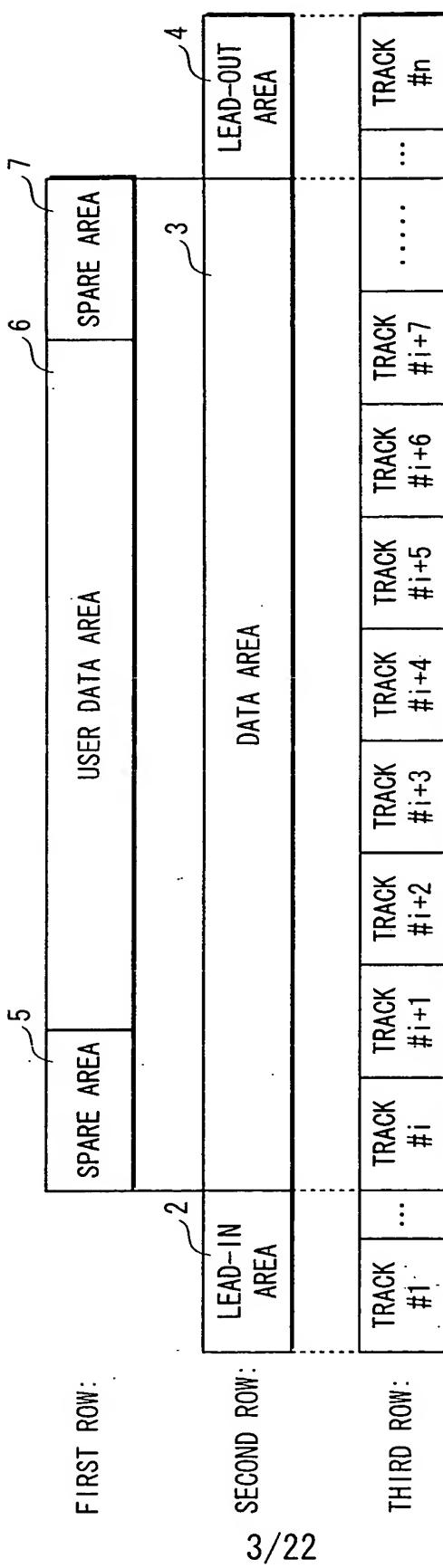
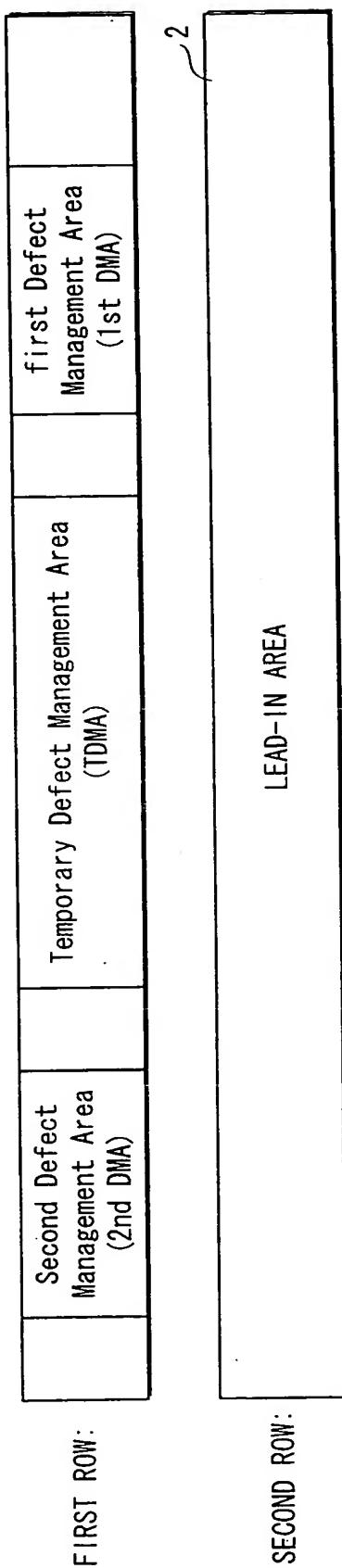


FIG. 4A



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FIG. 4B

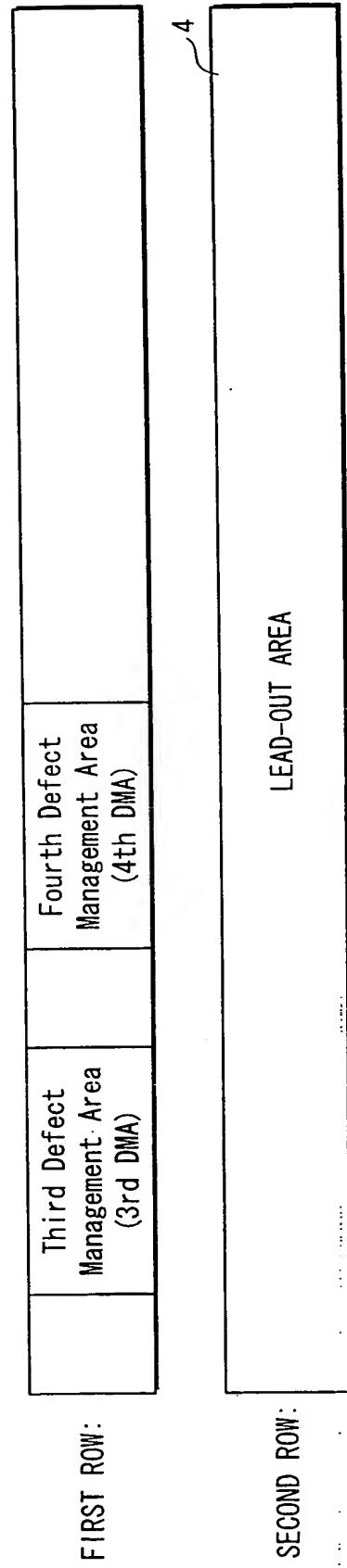


FIG. 5A

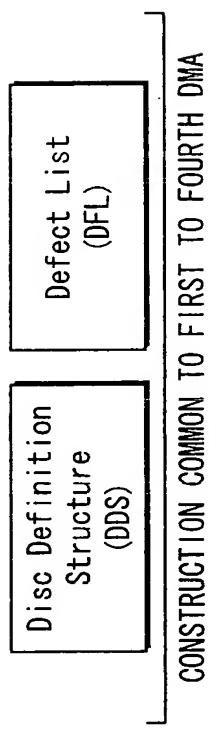


FIG. 5B

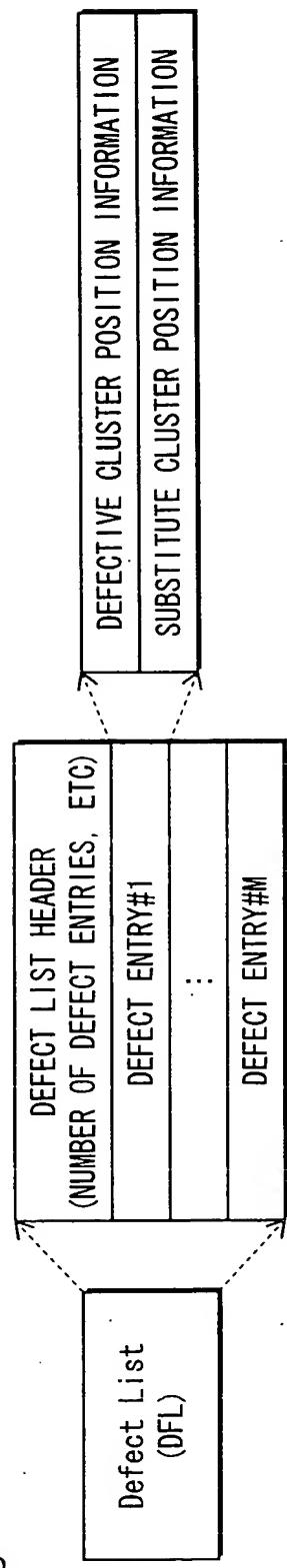


FIG. 5C

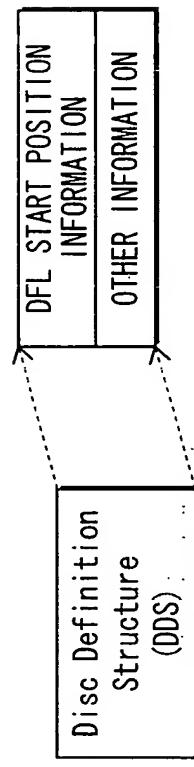
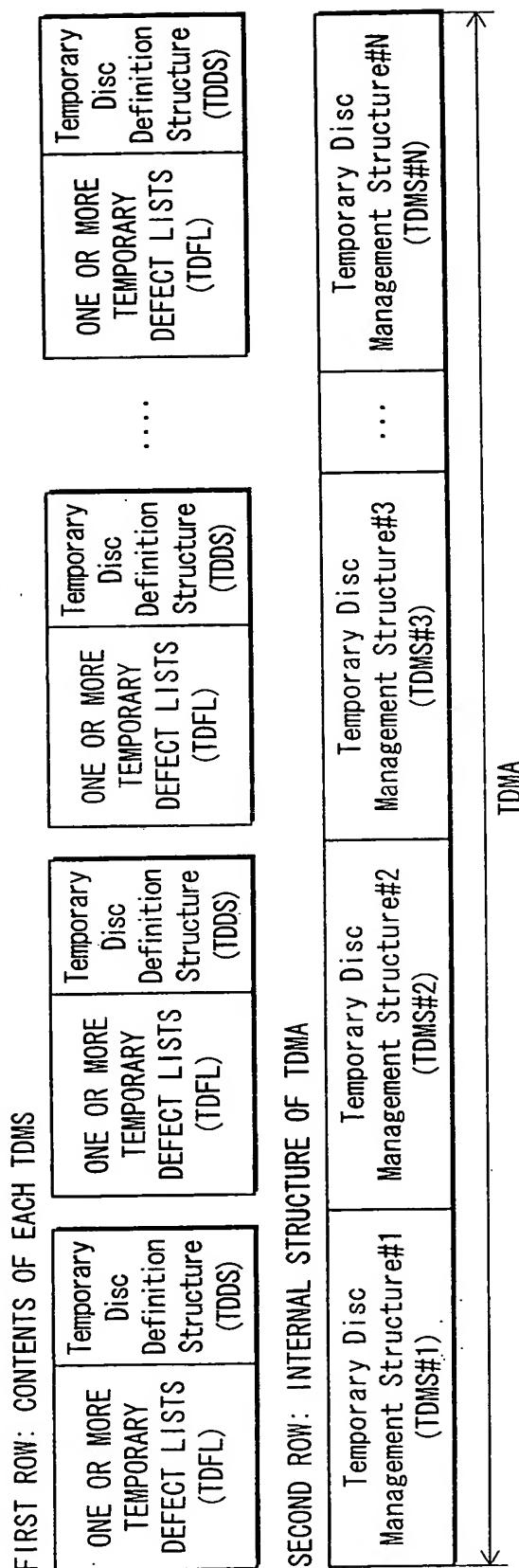


FIG. 6



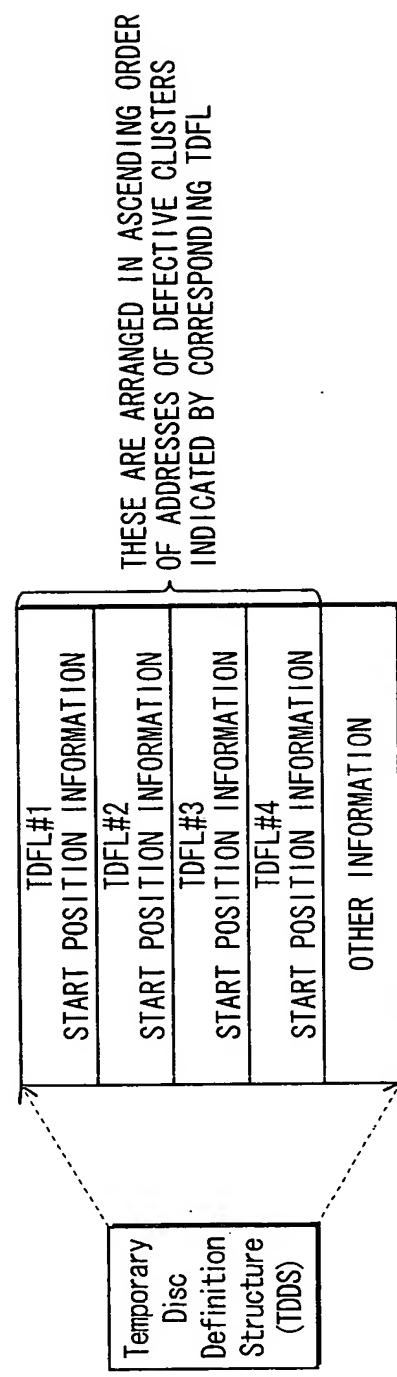
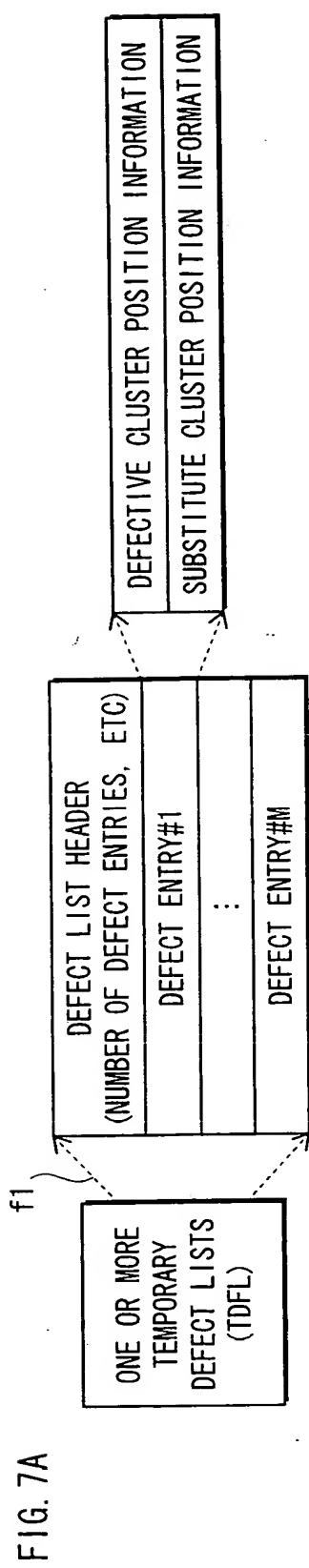
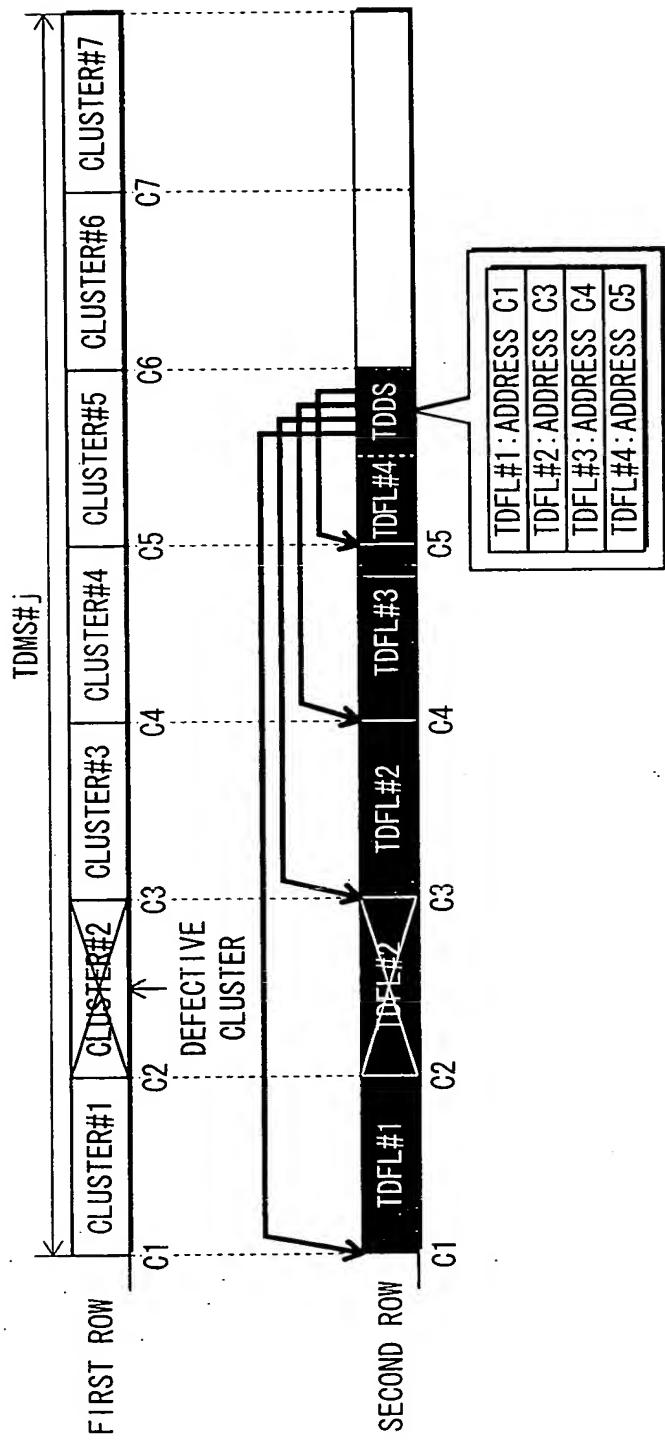


FIG. 8



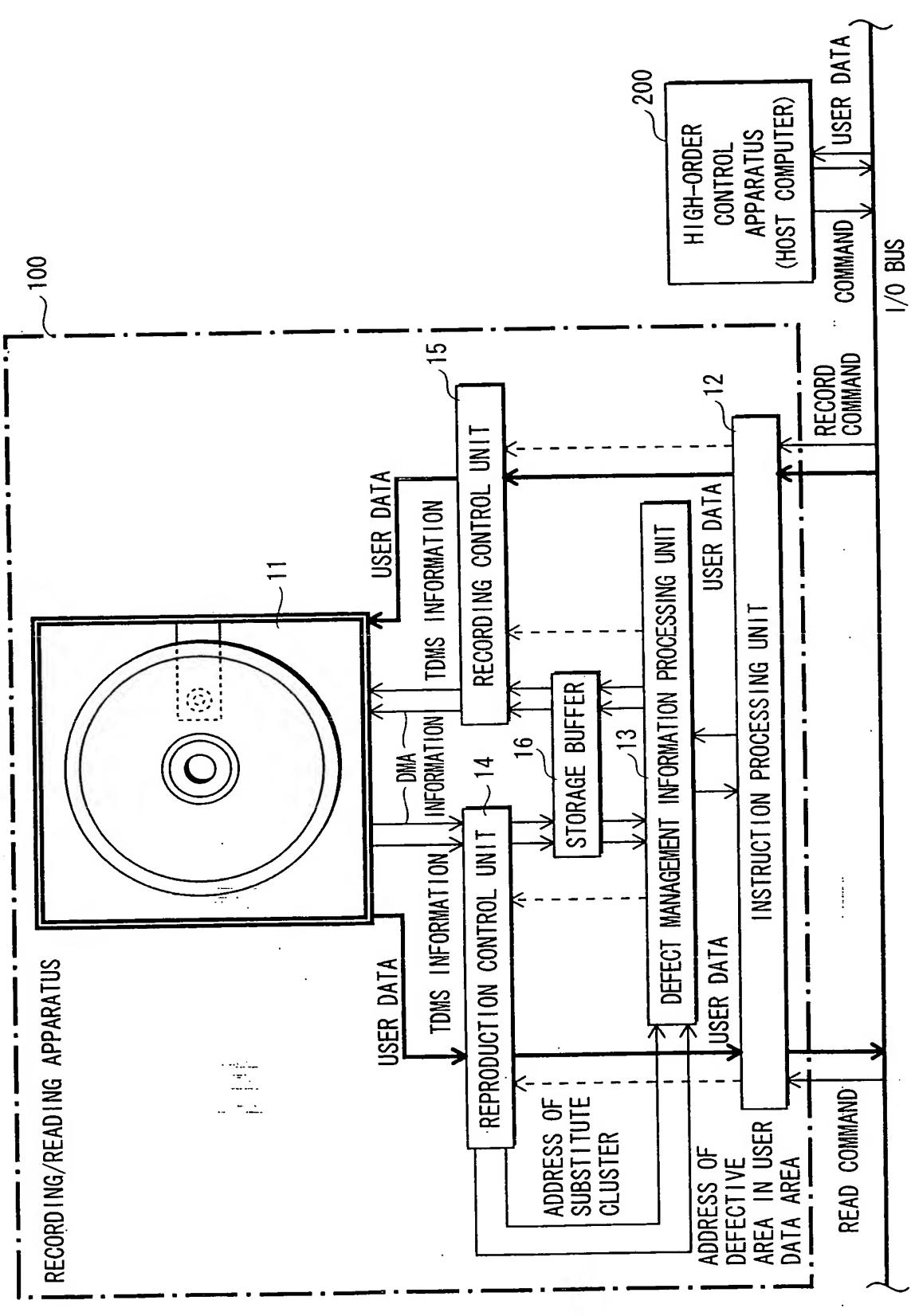
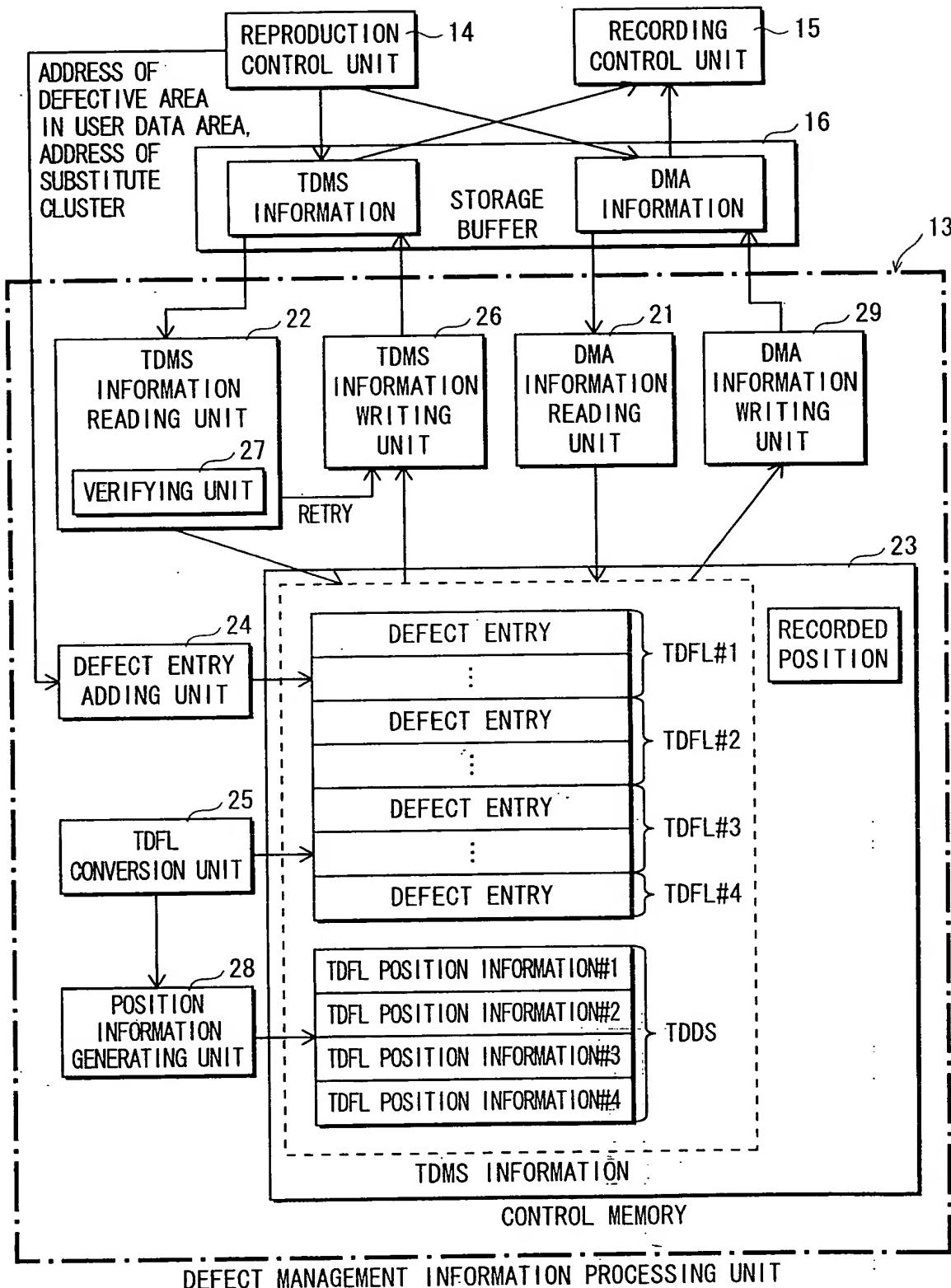
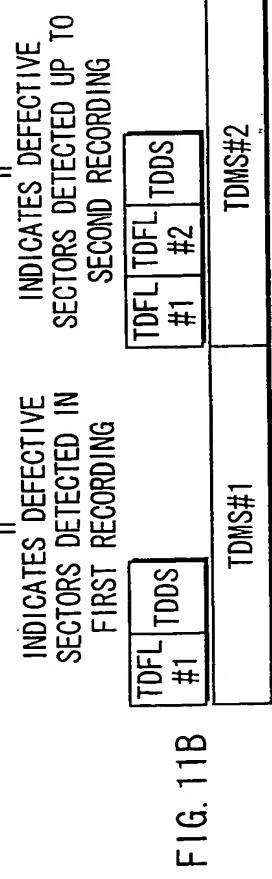
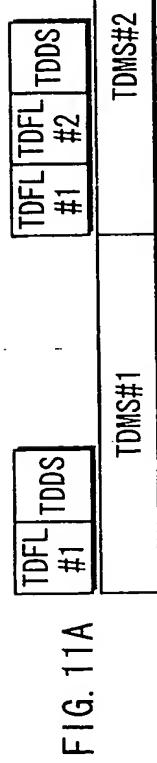
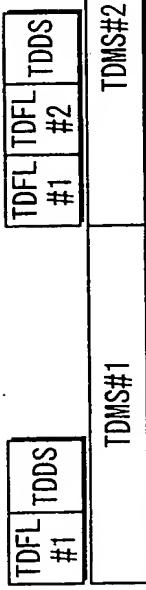


FIG. 10

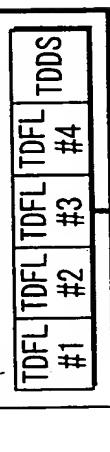




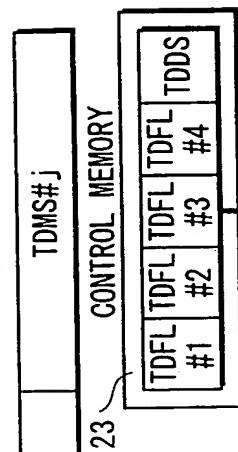
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**FIG. 11C**



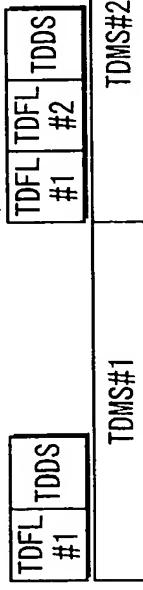
23 CONTROL MEMORY



23 CONTROL MEMORY

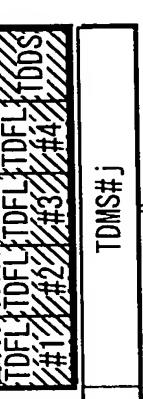


23 CONTROL MEMORY

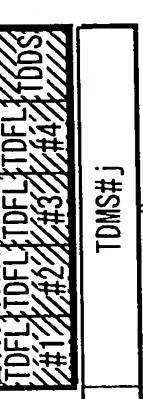


INDICATES DEFECTIVE SECTORS DETECTED UP TO j TH RECORDING

**FIG. 11D**



23 CONTROL MEMORY



23 CONTROL MEMORY

FIG. 12A CONTROL MEMORY 23

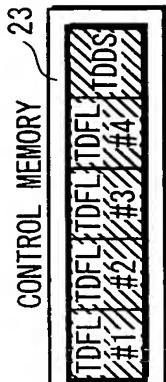
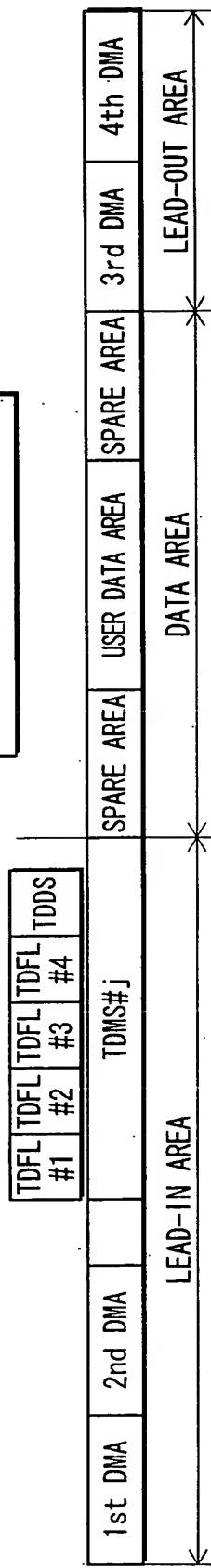


FIG. 12B

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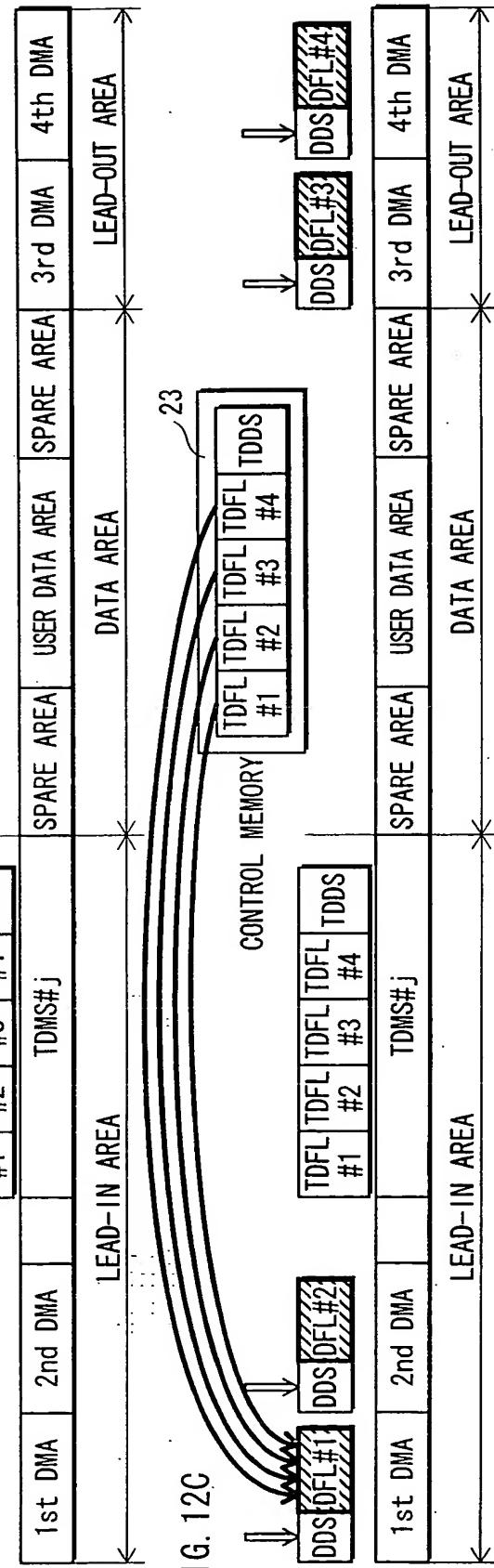
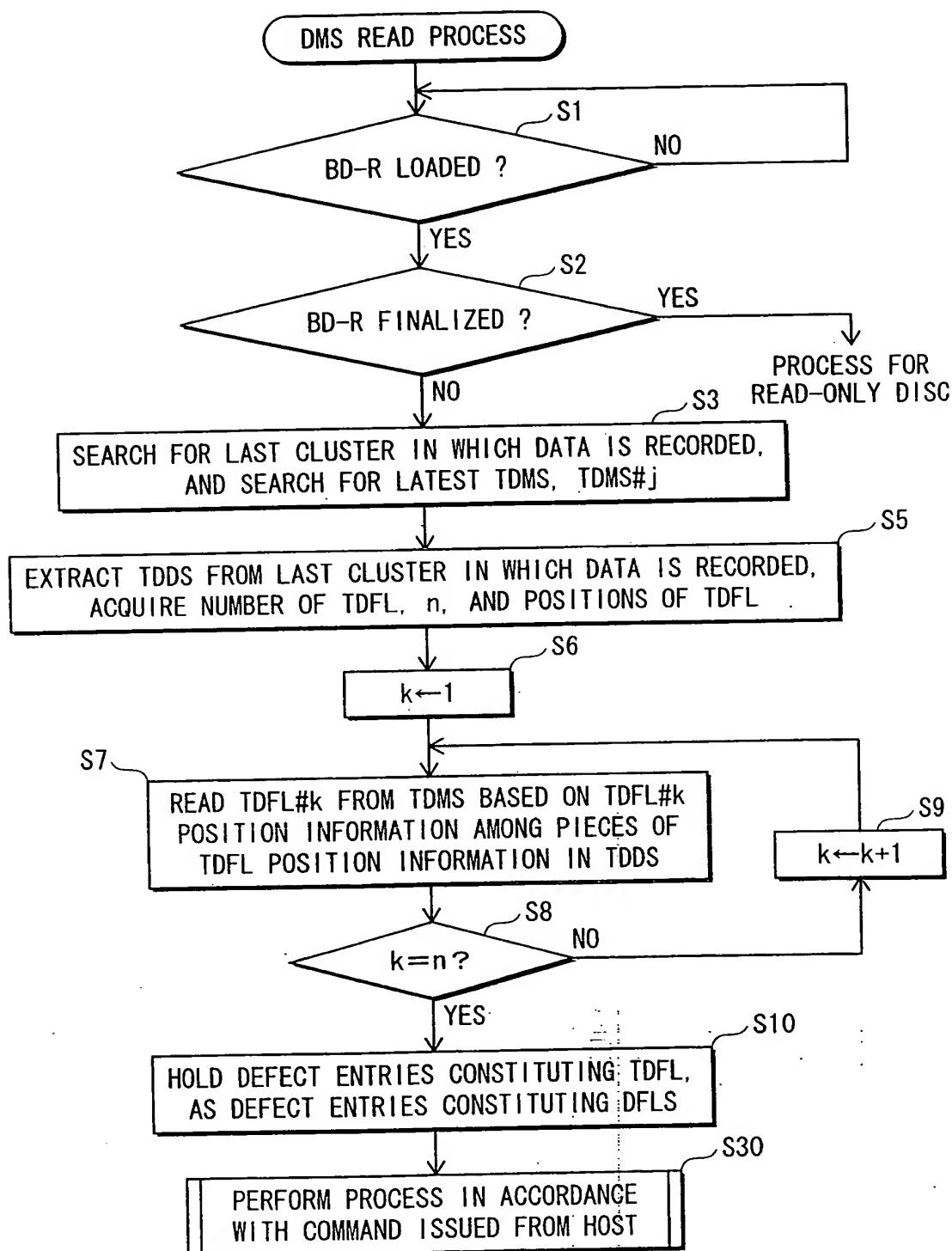


FIG. 12C

FIG. 13



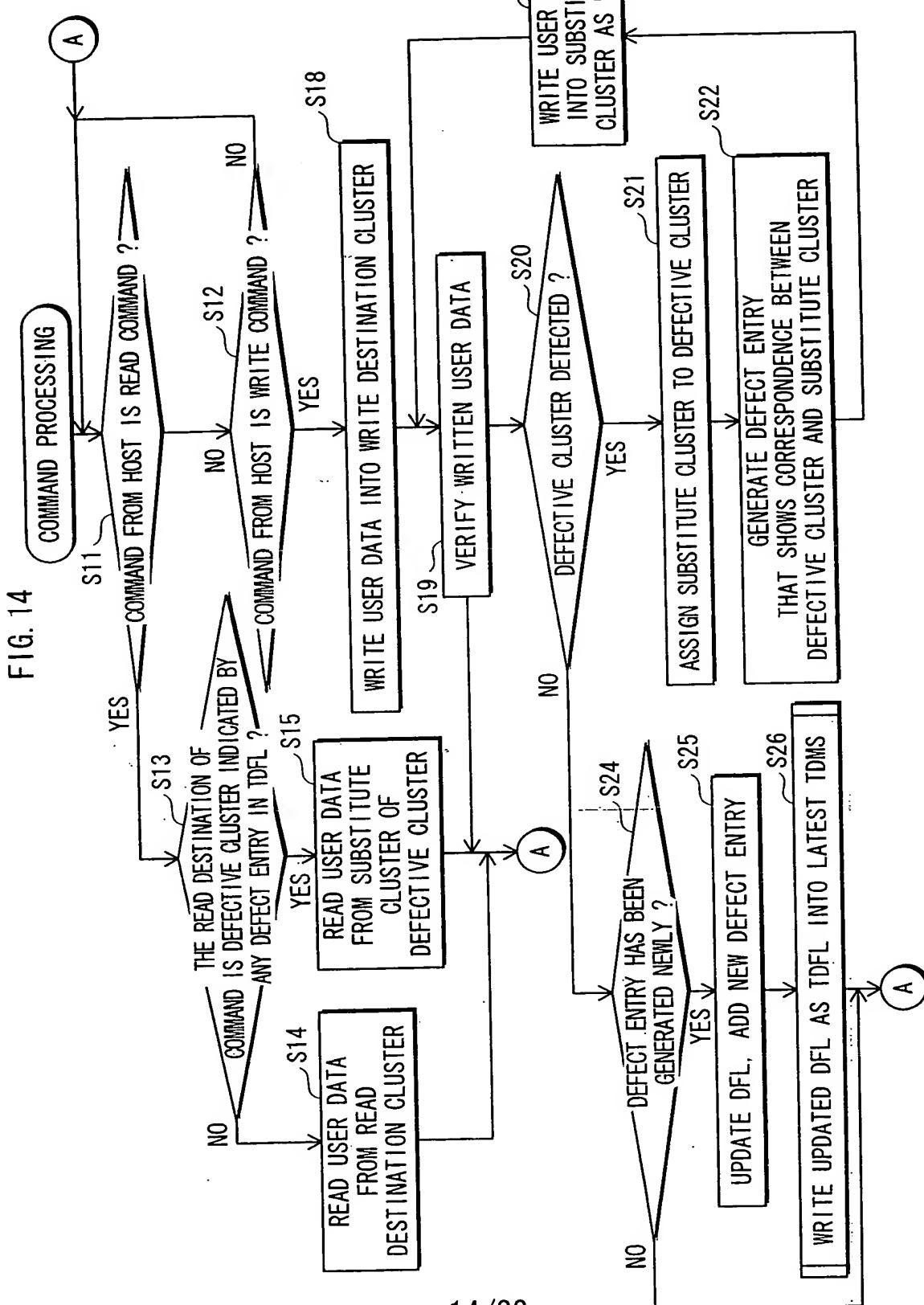
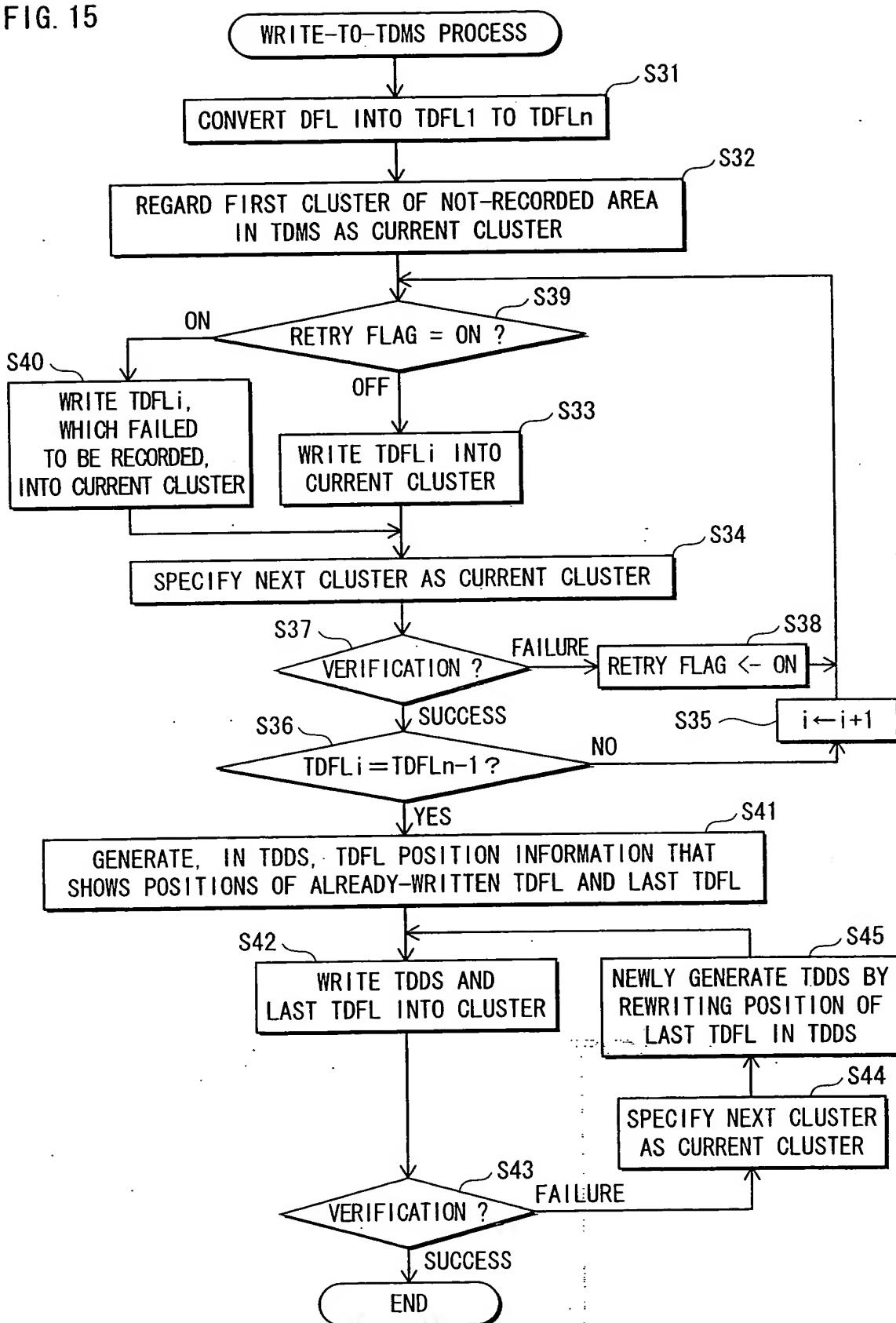


FIG. 15



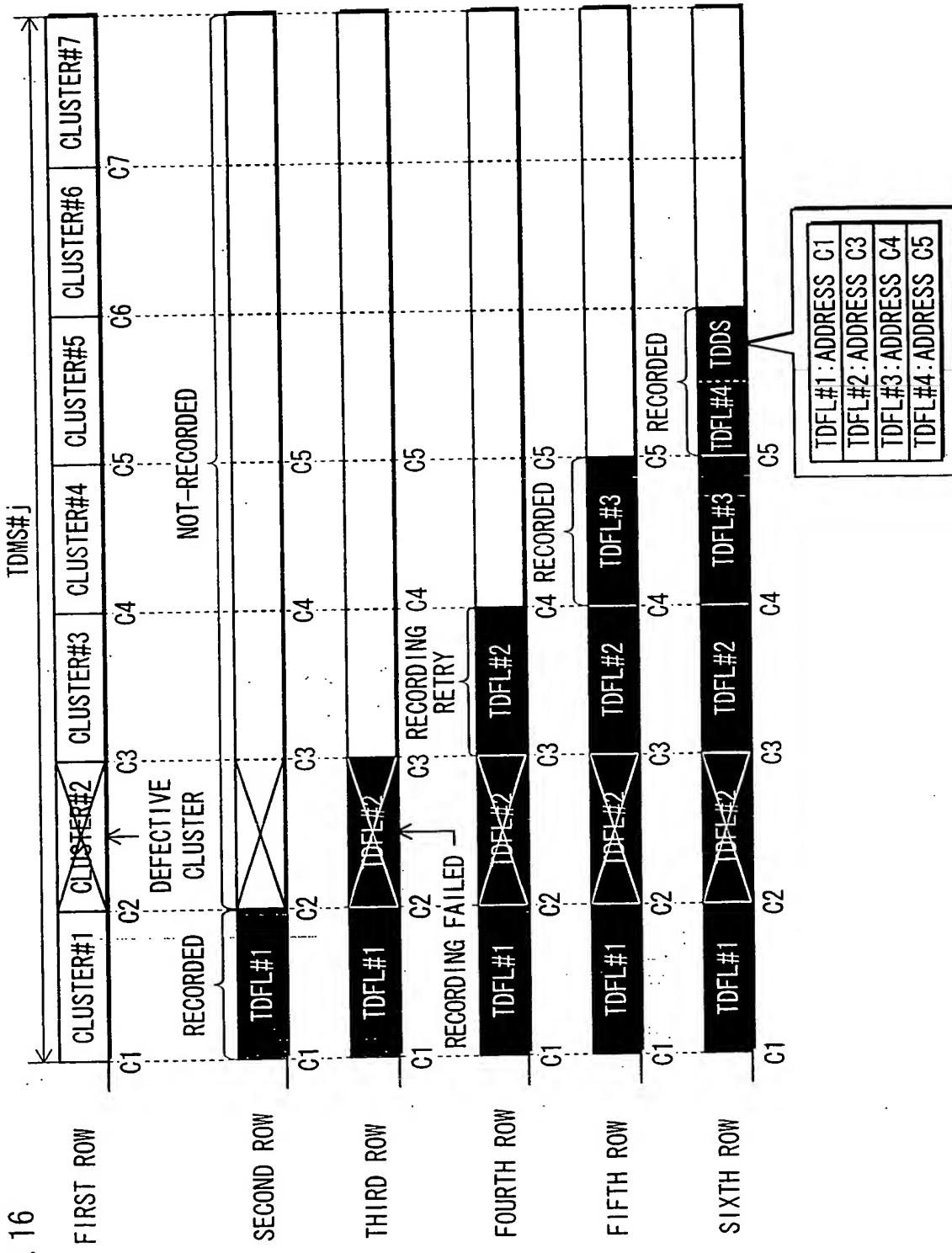


FIG. 17

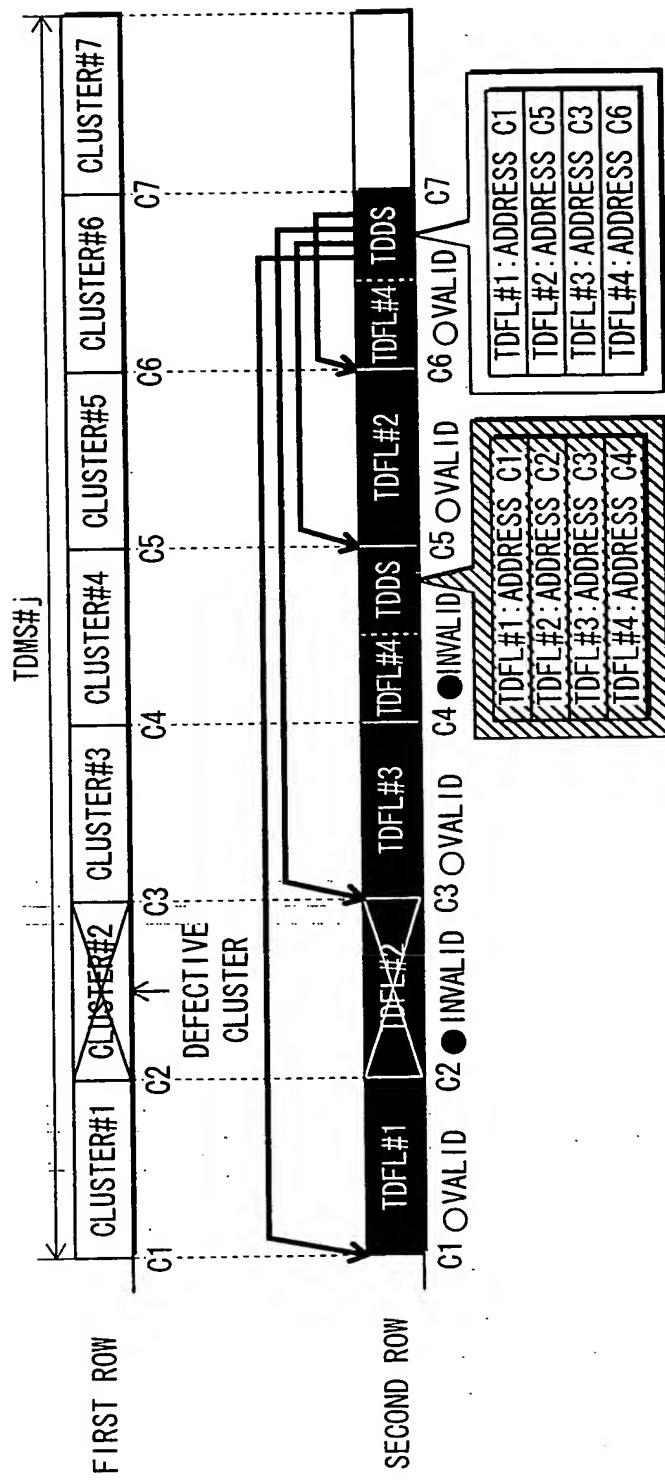


FIG. 18

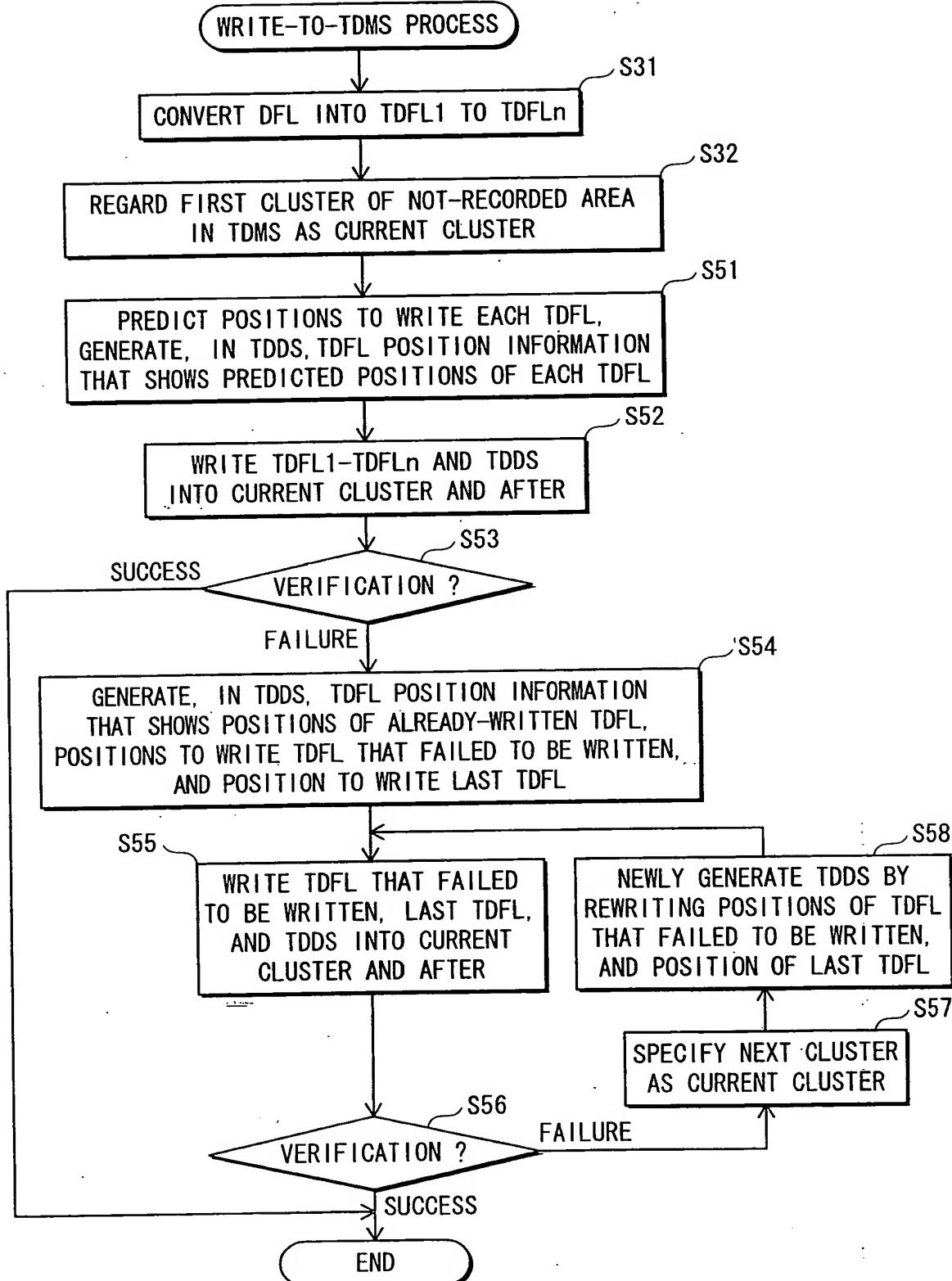


FIG. 19

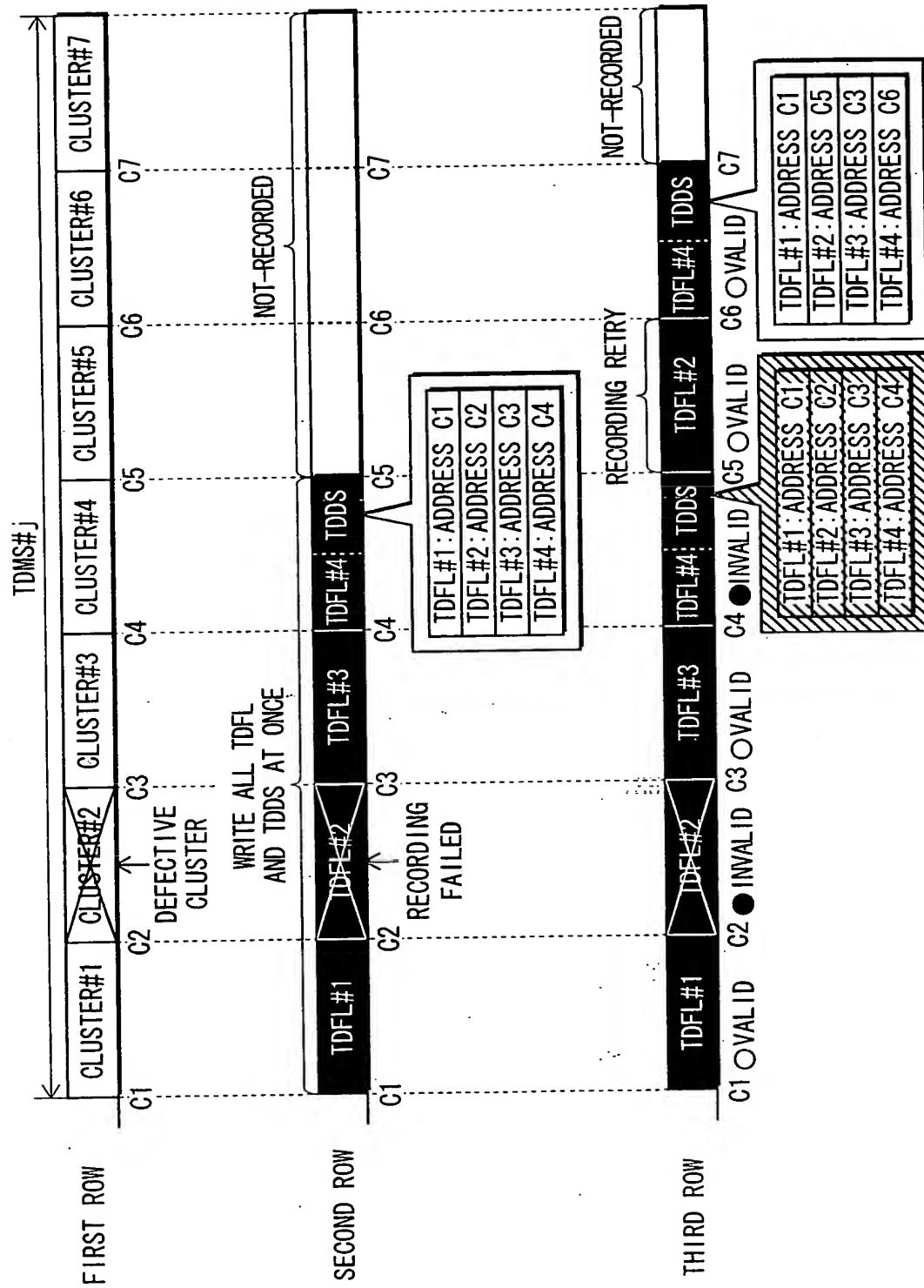


FIG. 20

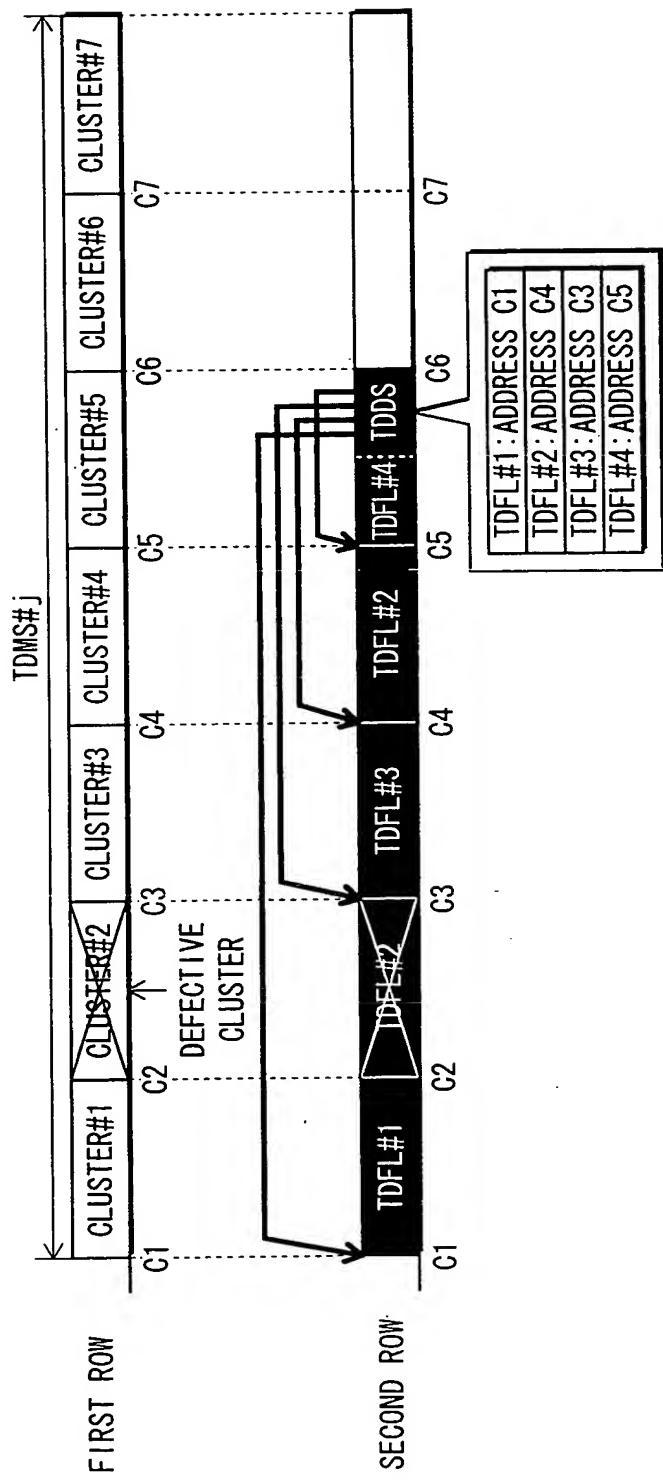


FIG. 21

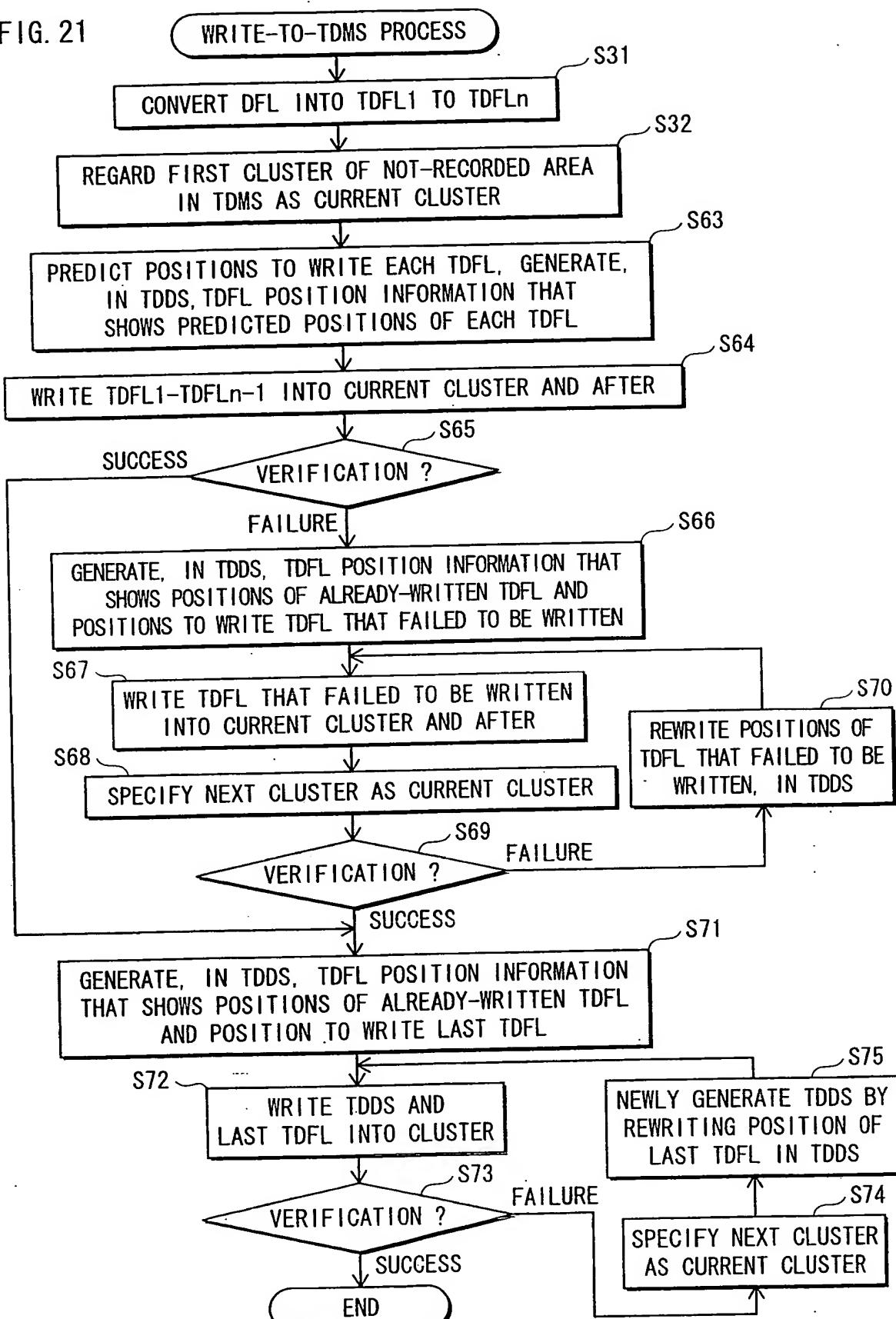


FIG. 22

